Commutativity Condition Refinement

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Abstract—We present a technique for automatically generating commutativity conditions from (abstract-level) data-structure specifications. We observe that one can pose the commutativity question in a way that does not introduce additional quantifiers, via a mechanized lifting of a (potentially partial) specification to an equivalent total specification. We then describe an algorithm that iteratively refines an under-approximation of the commutativity (and non-commutativity) condition for two data-structure methods \( m \) and \( n \) into an increasingly precise version. Our algorithm terminates if/when the entire state space has been considered, and can be aborted at any time to obtain a partial, sound commutativity condition. We prove soundness, relative completeness, describe how to obtain input predicates, and discuss heuristics to improve qualitative aspects of our algorithm’s output.

We have implemented our technique in a tool called SERVOIS, which uses an SMT solver. We show that we can automatically generate symbolic commutativity conditions for a range of data structures including Set, HashTable, Accumulator, Counter, and Stack. This work has the potential to impact a variety of contexts, in particular, multicore software where it has been realized that commutativity is at the heart of increased concurrency.

I. INTRODUCTION

Recent decades have seen the development of a variety of paradigms to exploit the opportunity for concurrency in multicore architectures, including parallelizing compilers [22], speculative execution (e.g. transactional memory [11]), futures, etc. It has been shown, across all of these domains, that understanding the commutativity of concurrent data-structure operations provides a key avenue to improved performance [6] as well as ease of verification [15], [14].

Intuitively, linearizable data-structure operations that commute can be executed concurrently because their effects don’t interfere with each other in a harmful way. When using a (linearizable) HashTable, for example, knowledge that \( \text{put}(x,'a') \) commutes with \( \text{get}(y) \) provided that \( x \neq y \) enables significant parallelization opportunities as both can be performed concurrently.

Commutativity conditions are an important part of the concurrent programming toolkit, but they are tedious to specify manually and require nontrivial and error-prone reasoning. Recent advances have been made on verification of commutativity conditions [13], as well as attempts at synthesis based on random interpretation [3] or dynamic profiling [24]. Thus far, however, generating commutativity conditions automatically has been largely overlooked.

We present the first known technique for automatic refinement of commutativity conditions. We build on a vast body of existing research, extending over the last five decades, on specification and representation of abstract data types (ADTs) in terms of logical \((\text{Pre}_m,\text{Post}_m)\) specifications [12], [9], [10], [4], [20], [17] of methods \( m, n, \ldots \).

Our technique generates a logical commutativity condition \( \varphi_m^n \) for each pair \( m, n \) that specifies when method \( m \) commutes with method \( n \). We first observe that one can pose the commutativity question in a way that does not introduce additional quantifiers via a mechanized lifting of a (potentially partial) method specification to an equivalent total specification.

Next, the predicate vocabulary for expressing the condition \( \varphi_m^n \) is populated automatically by deriving atoms, used to describe the pre/post footprint of operations, from the transition system’s specification. Intuitively, since these atoms suffice to capture the effect of an operation, they may also capture the conditions under which the effects of two operations do, or do not, conflict.

Based on the predicate vocabulary, our algorithm iteratively relaxes an under-approximation of the commutativity (and non-commutativity) condition, starting from \textit{false}, into an increasingly precise version. At each step, we conjunctively subdivide the state space into regions, searching for areas where \( m \) and \( n \) commute and where they don’t. Throughout this recursive process, we accumulate the commutativity condition as a growing disjunction of these regions.

We have proved that the algorithm is sound, and can also be aborted at any time to obtain a partial commutativity condition. This is often useful in practice, as partial commutativity conditions typically outperform alternate implementations that don’t use commutativity. We also show that if the algorithm terminates without being aborted, it results in a complete specification and provide useful conditions under which termination is guaranteed.

We have implemented our approach as the SERVOIS\(^*\) tool, which is able to generate commutativity conditions for various popular data structures, including Set, Counter, HashTable and Stack. The conditions typically combine multiple theories, such as sets, integers, arrays, etc. As such, we have implemented our technique atop the SMT solver CVC4 [5].

This paper makes the following principal contributions:

- Techniques to lift partial transition specifications and iteratively refine commutativity conditions (Sec. IV and V).

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\( \ast \text{http://cs.nyu.edu/~kshitij/projects/servois/} \)
• Proof of soundness and relative completeness (Sec. VI).
• Automated extraction of base formula terms (Sec. VII).
• SMT-based implementation (Sec. VII).
• Demonstrated efficacy for several key data structures including Set, HashTable, Accumulator, Counter, and Stack (Sec. VII).

Other related work. Both Aleen and Clark [3] and Tripp et al. [25] identify sequences of actions that commute (via random interpretation and dynamic analysis, respectively) but neither technique yields an explicit commutativity condition. Kulkarni et al. [16] point out that varying degrees of commutativity specification precision are useful. Kim and Rinard [13] use Jahob to verify manually specified commutativity conditions of several different linked data structures. Data-structure commutativity specifications are also found in dynamic analysis techniques [8].

More distantly related is the work on synthesizing program implementations, such as CEGIS [23], and synchronization synthesis [28], [27]. Aderhold [2] describes a method to synthesize, or extract, induction axioms from programs with indirect recursive calls (e.g. algorithms on data structures). Leino [18] explains the process by which verification conditions are generated for the object-oriented language Spec#. Leino’s conditions target SMT solvers.

II. Overview

Motivation. Specifying commutativity conditions is nontrivial. Not only is this task burdensome since it has to be done pairwise for all methods (i.e. quadratic), but even if there are few operations, commutativity conditions are often subtle.

As an illustration, consider the Set ADT, whose state consists of a single variable, \( S \), that stores an unordered collection of unique elements. We focus on two operations:

- `contains(x) / bool`, which performs a side-effect-free check whether the element \( x \) is in \( S \); and
- `add(y) / bool`, which adds \( y \) to \( S \) if it is not already in there and returns `true`, or otherwise returns `false`.

`add` and `contains` clearly commute if they refer to different elements in the set. There is, however, another case that is less obvious: `add` and `contains` commute if they refer to the same element \( e \), as long as in the prestate \( e \in S \). In this case, in both orders of execution `add` and `contains` leave the set unmodified and return `false` and `true`, respectively.

Capturing precise conditions such as these by hand, and doing so for many pairs of operations, is tedious, error prone, and benefits from automation.

Iterative Refinement Algorithm. The algorithm we describe in this paper automatically produces a precise logical formula \( \varphi \) that captures this commutativity condition, i.e. the disjunction of the two cases above:

\[ \varphi \equiv x \neq y \lor (x = y \land x \in S). \]

The algorithm also generates the conditions under which the methods do not commute:\n
\[ \tilde{\varphi} \equiv x = y \land x \notin S. \]

This is precise since \( \varphi \) is the negation of \( \tilde{\varphi} \).

The main thrust of the algorithm is to recursively subdivide the state space via predicates until, at the base case, regions are found that are either entirely commutative or else entirely non-commutative. As in the example above, the conditions we incrementally generate are denoted \( \varphi \) and \( \tilde{\varphi} \), respectively.

We illustrate how our algorithm proceeds on the running example in Figure 1. We denote by \( H \) the logical formula that describes the current state space at a given recursive call. As expected, we begin with \( H_0 = \text{true} \), \( \varphi = \text{false} \), and \( \tilde{\varphi} = \text{false} \). There are essentially three cases for a given \( H \): (i) \( H \) describes a precondition for \( m \) and \( n \) in which \( m \) and \( n \) always commute; (ii) \( H \) describes a precondition for \( m \) and \( n \) in which \( m \) and \( n \) never commute; or (iii) neither of the above. The latter case drives the recursive algorithm to subdivide the region by choosing a new predicate.

In Section VI, we state the formal guarantees of the algorithm. We have proved that it is sound, i.e. it produces sound commutativity conditions, even if aborted. Soundness is guaranteed even if the ADT description involves undecidable theories. We further show that termination implies completeness, and specify broad conditions that imply termination (i.e. relative completeness).

Challenges. While the algorithm, as outlined so far, executes a relatively standard refinement loop, there are interesting challenges that are implicit in its description.

The first challenge pertains to the complexity and decidability of the validity queries discharged to the supporting SMT solver. In particular, if the query contains alternating quantification (specifically the \( \forall \exists \) pattern), then decidability is lost. In our technique, we avoid introducing additional quantification by construction. Hence, if the underlying theories are decidable, then the queries we pose to the SMT solver are guaranteed to also be decidable. We achieve this mechanically by symbolically completing the (potentially) partial transition system into a total system through the addition of a new \( \text{Err} \) state. \( \text{Err} \) becomes the image of states without successors. This modified encoding, formalized in Section IV, ensures that universal quantification suffices without the need to introduce additional quantification.

Next, there is the critical question of which predicates
to range over during the iterative refinement process. If the predicate vocabulary is not sufficiently expressive, then the algorithm would not be able to converge on precise commutativity and non-commutativity conditions. In Section VII, we provide a mechanized solution to this problem, whereby the predicate vocabulary is populated with the atoms that occur in the transition relations’ Pre and Post formulas. As we demonstrate in Section VII, this strategy works extremely well in practice. An intuitive explanation is that the Pre and Post formulas suffice to express the footprint of an operation, and so the atoms comprising them are an effective vocabulary to express when operations do, or do not, interfere.

A final challenge, having fixed the predicate vocabulary, is to prioritize the predicates. This choice essentially drives the iterative refinement loop, and so it controls not only the algorithm’s performance, but also the quality (or conciseness) of the resulting conditions. Our choice of next predicate \( p \) is governed by two requirements. First, for progress, \( pl \vdash \lnot p \) must eliminate the counterexamples to commutativity/non-commutativity due to the last iteration (where previously selected predicates ensure the same for their respective counterexamples). This may still leave multiple choices, and we propose two heuristics with different trade-offs to break ties. We discuss this in more detail in Section VII.

In summary, the following diagram illustrates the overall flow of our automated algorithm, including the components discussed above to generate useful predicates (PGEN), complete the transition relation (LIFT), and choose the next predicate along the refinement process (CHOOSE box inside REFINE).

In the diagram, we denote the total version of the \( \text{Pre}_m/\text{Post}_n \) specifications as \( \text{Pre}_m/\text{Post}_n \), the generated commutativity condition as \( \varphi^m_n \) and the non-commutativity condition \( \neg \varphi^m_n \).

### III. Preliminaries

**States and actions.** We will work with a state space denoted \( \Sigma \), with decidability equality and a set of actions \( A \). For each \( \alpha \in A \), we have a transition function \( [\alpha] : \Sigma \rightarrow \Sigma \). We denote a single transition as \( \sigma \xrightarrow{\alpha} \sigma' \) and we assume that each such action are completes in finite time. Let \( \mathfrak{T} \equiv (\Sigma, A, (\cdot \mid \cdot)) \).

**Definition III.1** (Action commutativity [8]). We say that two actions \( \alpha_1 \) and \( \alpha_2 \) commute, denoted \( \alpha_1 \bowtie \alpha_2 \), provided that \( [\alpha_1] \circ ([\alpha_2] \circ [\alpha_1]) \).

Note that \( \bowtie \) is with respect to \( \mathfrak{T} = (\Sigma, A, (\cdot \mid \cdot)) \).

Our formalism, implementation, and evaluation all extend to a more fine-grained notion of commutativity: an asymmetric version called left-movers and right-movers [19] where a method commutes in one direction and not the other. For ease of presentation, the formal detail in the body of this paper discusses only commutativity, but a discussion of how our technique generalizes can be found in Appendix A. Also, in our evaluation (Section VII) we show left-right-mover conditions that were generated by our implementation.

**Methods.** An action \( \alpha \in A \) is of the form \( m(\bar{x})/\bar{r} \) where \( m, \bar{x} \) and \( \bar{r} \) are called a method, arguments and return values respectively. For actions corresponding to a method \( n \), we use \( \bar{y} \) for arguments and \( \bar{s} \) for return values. In our context, the set of methods will be finite, inducing a finite partitioning of \( A \). We refer to an action, say \( m(\bar{x})/\bar{r} \), as corresponding to method \( m \). The set of actions corresponding to a specific method \( m \), denoted \( A_m \), might be infinite as the arguments and return values may be infinite.

**Definition III.2** (Method commutativity). For \( m \) and \( n \),

\[
m \bowtie n \equiv \forall \bar{x} \bar{y} \bar{r} \bar{s}. \ m(\bar{x})/\bar{r} \bowtie n(\bar{y})/\bar{s}
\]

Above we have quantified over all actions corresponding to \( m \) and \( n \). That is, the quantification \( \forall \bar{x} \bar{r} \) means \( \forall m(\bar{x})/\bar{r} \in A_m \).

**Abstract specifications.** We describe the actions of a (potentially partial) method \( m \) symbolically, as a pre-condition \( \text{Pre}_m \) and post-condition \( \text{Post}_m \). Pre-conditions are logical formulae over method arguments and the initial state, and post-conditions are over method arguments, and return values, initial state and final state:

\[
\left[ \text{Pre}_m \right] : \bar{x} \rightarrow \Sigma \rightarrow \text{Prop}
\]
\[
\left[ \text{Post}_m \right] : \bar{x} \rightarrow \bar{r} \rightarrow \Sigma \rightarrow \Sigma \rightarrow \text{Prop}
\]

If we have \( \text{Pre}/\text{Post} \)-conditions \( \left( \text{Pre}_m, \text{Post}_m \right) \) for every method \( m \), then we define a transition system \( \mathfrak{T} = (\Sigma, A, (\cdot \mid \cdot)) \) such that \( \sigma \xrightarrow{m(\bar{x})/\bar{r}} \sigma' \) iff \( \left[ \text{Pre}_m \right] \bar{x} \sigma \) and \( \left[ \text{Post}_m \right] \bar{x} \bar{r} \sigma' \).

Since our approach works on deterministic transition systems, we have implemented a check (discussed in Section VII) that ensures the input transition system is deterministic. Deterministic specifications were sufficient to model the Set, HashTable, Accumulator, Counter, and Stack, which is unsurprising given the inherent difficulty of creating efficient concurrent implementations of nondeterministic operations, whose effects are hard to characterize. We believe it may be possible to reduce nondeterministic data-structure methods to deterministic ones through symbolic partial determination [1], [7], but we leave this as future work.

**Logical commutativity formulae.** We will work with, and generate, a commutativity condition for methods \( m \) and \( n \) as logical formulae over initial states and the arguments/return values of the methods. We denote a logical commutativity formula as \( \varphi \) and assume a decidable interpretation of formulae: \( [\varphi] : (\sigma, \bar{x}, \bar{y}, \bar{r}, \bar{s}) \rightarrow \mathbb{B} \). (We tuple the arguments for brevity.) The first argument is the initial state. Commutativity pre- and mid-conditions can also be written [13] but here, for simplicity, we focus only pre-conditions. Throughout this paper, we may write \( [\varphi] \) as, simply, \( \varphi \) when it is clear from context that \( \varphi \) is meant to be interpreted.
We say that $\varphi^n_m$ is a sound commutativity (and $\hat{\varphi}^n_m$ a non-commutativity, resp.) condition for $m$ and $n$ provided that

\[
\forall \sigma \bar{x} \bar{y} \bar{s}. \quad \left[ \varphi^n_m \right] \sigma \bar{x} \bar{y} \bar{s} \Rightarrow m(\bar{x})/\bar{r} \triangleright n(\bar{y})/\bar{s}.
\]

**Trouble with existential quantification.** Notice that proving commutativity of methods $m$ and $n$ via Definition III.2 requires showing equivalence between different compositions of potentially non-surjective functions. That is, $\langle \alpha_1 \rangle \circ \langle \alpha_2 \rangle = \langle \alpha_1 \rangle \circ \langle \alpha_1 \rangle$ if and only if:

\[
\forall \sigma_0 \, \sigma_1 \sigma_2. \quad \langle \alpha_1 \rangle \sigma_0 = \sigma_1 \land \langle \alpha_2 \rangle \sigma_1 = \sigma_2 \Rightarrow \exists \sigma_3. \quad \langle \alpha_2 \rangle \sigma_0 = \sigma_3 \land \langle \alpha_1 \rangle \sigma_3 = \sigma_2. \\
\text{ (and a similar case for the other direction)}
\]

Even when the transition relation can be expressed in a decidable theory, because of $\forall \exists$ quantifier alternation (which is undecidable in general), any procedure requiring such a check would be incomplete. SMT solvers are particularly poor at handling such constraints. In the next section, we describe a mechanized transformation of the transition system that allows us to encode commutativity as a universal formula with no quantifier alternation.

**IV. LIFTING FOR COMMUTATIVITY**

In this section, we describe a simple transformation on transition systems to a lifted domain, and a definition of commutativity in the lifted domain $m \bowtie n$ that is equivalent to Definition III.2. This new definition requires only universal quantification, and as such, is better suited to automation in SMT-based algorithms (e.g. Section V).

**Definition IV.1 (Lifted transition function).** For a given $\Sigma = (\Sigma, A, \{ [\cdot] \})$, we LIFT $\Sigma$ to transition system $\hat{\Sigma} = (\Sigma, A, \{ [\cdot] \})$ where $\Sigma = \Sigma \cup \{ Err \}$, $Err \notin \Sigma$, and $\langle [\alpha] \rangle : \Sigma \rightarrow \Sigma$ is:

\[
\langle [\alpha] \rangle \hat{\sigma} = \begin{cases} 
Err & \text{if } \hat{\sigma} = Err \\
\langle [\alpha] \rangle \sigma & \text{if } \hat{\sigma} \in \text{dom}(\langle [\alpha] \rangle) \\
Err & \text{otherwise}
\end{cases}
\]

Intuitively, $\langle [\alpha] \rangle$ wraps $\langle [\alpha] \rangle$ so that $Err$ loops back to $Err$, and the (potentially partial) $\langle [\alpha] \rangle$ is made to be total, by mapping elements to $Err$ when they are undefined in $\langle [\alpha] \rangle$. Note that it is not necessary to lift the actions (or, indeed, the methods), but only the states and transition function. Once lifted, for a given state $\sigma_0$, the question of some successor state becomes equivalent to all successor states because there is exactly one successor state.

**Abstraction.** Our prior pre-/post-conditions ($Pre_m, Post_m$) were suitable for specifications of potentially partial transition systems. We now describe a translation of to a new ($\hat{Pre}_m, \hat{Post}_m$) that induces a corresponding lifted transition system that (i) is surjective and (ii) remains deterministic. These lifted specifications have types over lifted state spaces:

\[
\hat{Pre}_m : \hat{x} \rightarrow \hat{\Sigma} \rightarrow \text{Prop} \\
\hat{Post}_m : \hat{x} \rightarrow \hat{r} \rightarrow \hat{\Sigma} \rightarrow \hat{\Sigma} \rightarrow \text{Prop}
\]

Our tool performs lifting automatically via translation from a ($Pre_m, Post_m$) specification to:

\[
\hat{Pre}_m(\hat{x}, \hat{\sigma}) \equiv \text{true} \\
\hat{Post}_m(\hat{x}, \hat{r}, \hat{\sigma}, \hat{\sigma}') \equiv \\
\forall \hat{\sigma}. \quad \hat{\sigma} = \text{Err} \land \hat{\sigma}' = \text{Err} \\
\lor \forall \hat{\sigma}. \quad \hat{\sigma} \neq \text{Err} \land \hat{Pre}_m(\hat{x}, \hat{\sigma}) \land \hat{\sigma}' \neq \text{Err} \land \hat{Post}_m(\hat{x}, \hat{r}, \hat{\sigma}, \hat{\sigma}') \\
\lor \forall \hat{\sigma}. \quad \hat{\sigma} \neq \text{Err} \land \neg \hat{Pre}_m(\hat{x}, \hat{\sigma}) \land \hat{\sigma}' = \text{Err}
\]

We abuse notation, giving $\hat{\sigma}$ as an argument to $\hat{Pre}_m$, etc.

It is easy to see that the lifted transition system induced by this translation ($\hat{\Sigma}, \{ [\cdot] \}$) is of the form given in Definition IV.1. In the Appendix, we show how our tool transforms a counter specification (Apx. B) into an equivalent lifted version (Apx. C). Notice that this specification is now a total transition system: $\hat{Pre}_m = \text{true}$. 

A. **Posing commutativity with only universal quantifiers**

With the above lifting, we can pose commutativity as a universal question (modulo quantifiers in the underlying data-structure theory). We will use the notation $\bowtie$ to mean $\bowtie$ but over the lifted transition system $\hat{\Sigma}$. Notice, now that since $\bowtie$ is over total and deterministic transition functions, $\alpha_1 \bowtie \alpha_2$ it is equivalent to:

\[
\forall \sigma_0, \sigma_0 \neq \text{Err} \Rightarrow \langle [\alpha_2] \rangle \langle [\alpha_1] \rangle \sigma_0 = \langle [\alpha_1] \rangle \langle [\alpha_2] \rangle \sigma_0
\]

The equivalence above is state equality. Importantly, this is a universally quantified formula which translates to a ground satisfiability check in an SMT solver (modulo the theories used to model the data-structure).

**Theorem IV.1 (Lifted commutativity equivalence).** For methods $m$ and $n$, $m \bowtie n$ if and only if $m \bowtie n$.

**Proof.** Both directions are proved with classical reasoning, functional extensionality and case analysis on whether, for a given state $\sigma$, $\langle [\alpha] \rangle \sigma$ is defined or undefined. $\square$

B. **Checking whether ($H^m_n \Rightarrow m \bowtie n$) or ($H^m_n \Rightarrow m \bowtie n$)**

For a logical formula $H^m_n(\sigma, \hat{x}, \hat{y}, \hat{r}, s)$, given the lifting described above, we can check whether $H^m_n$ specifies conditions under which $m \bowtie n$ via an SMT query that does not introduce quantifier alternation. For brevity, we define the following syntactic sugar:

\[
\text{valid}(H^m_n \Rightarrow m \bowtie n) \equiv \text{valid} \left( \forall \hat{\sigma}_0. \quad H^m_n(\hat{\sigma}_0, \hat{x}, \hat{y}, \hat{r}, \hat{s}) \Rightarrow \sigma_0 \neq \text{Err} \Rightarrow m(\hat{x})/\hat{r} \bowtie n(\hat{y})/\hat{s} \right)
\]

Above we assume as a black box, an SMT solver providing valid. Here we have lifted the universal quantification within $\bowtie$ outside the implication.

We can similarly check whether $H^m_n$ is a condition under which $m$ and $n$ do not commute. First, we can define negative analogs of the commutativity definitions:

\[
\alpha_1 \bowtie n \alpha_2 \equiv \forall \hat{\sigma}_0. \quad \sigma_0 \neq \text{Err} \Rightarrow \langle [\alpha_2] \rangle \langle [\alpha_1] \rangle \sigma_0 \neq \langle [\alpha_1] \rangle \langle [\alpha_2] \rangle \sigma_0
\]

\[
m \bowtie n \equiv \forall \hat{x} \, \hat{y} \, \hat{r} \, \hat{s}. \quad m(\hat{x})/\hat{r} \bowtie n(\hat{y})/\hat{s}
\]
We thus define a check for when \( \varphi \) is a non-commutativity condition with the following syntactic sugar:

\[
\text{valid}(H_n \Rightarrow m \nabla n) \equiv \text{valid} \left( \forall \sigma_0 \exists \bar{x} \exists \bar{y} \exists \bar{s}, 
H_m(\sigma_0, \bar{x}, \bar{y}, \bar{s}) \Rightarrow \sigma_0 \neq \text{Err} \Rightarrow m(\bar{x})/\bar{r} \nabla n(\bar{y})/\bar{s} \right)
\]

V. Commutativity Condition Refinement

We now present an iterative refinement strategy that, when given a lifted abstract transition system, generates commutativity and non-commutativity conditions. In Section VI we discuss soundness and relative completeness.

The refinement algorithm symbolically searches the state space for regions where the operations commute (or do not commute) in a conjunctive manner, adding on one predicate at a time. We add each subregion \( H \) (described conjunctively) in which commutativity always holds to a growing disjunctive description of the commutativity condition \( \varphi \), and each subregion \( H \) in which commutativity never holds to a growing disjunctive description of the non-commutativity condition \( \hat{\varphi} \).

The algorithm in Figure 2 begins by setting \( \varphi = false \) and \( \hat{\varphi} = false \). \( \text{REFINE} \) begins a symbolic binary search through the state space \( H \), beginning with the entire state: \( H = true \). It also may use a collection of predicates \( \mathcal{P} \) (discussed later).

At each iteration, \( \text{REFINE} \) checks whether the current \( H \) represents a region of space for which \( m \) and \( n \) always commute: \( H \Rightarrow m \nabla n \). If so, \( H \) can be disjunctively added to \( \varphi \). It may, instead be the case that \( H \) represents a region of space for which \( m \) and \( n \) never commute: \( H \Rightarrow m \nabla n \). If so, \( H \) can be disjunctively added to \( \hat{\varphi} \). If neither of these cases hold, we have two counterexamples. \( \chi_c \) is the counterexample to commutativity, returned if the validity check on Line 2 fails. \( \chi_{nc} \) is the counterexample to non-commutativity, returned if the validity check on Line 4 fails.

We now need to subdivide \( H \) into two regions. This is accomplished by selecting a new predicate \( p \) via the \text{CHOOSE} method. For now, let the method \text{CHOOSE} and the choice of predicate vocabulary \( \mathcal{P} \) be parametric. \text{REFINE} is sound regardless of the behavior of \text{CHOOSE}, in Section VI we give the conditions on \text{CHOOSE} that ensure relative completeness, and in Section VII, we discuss our particular strategy. Regardless of what \( p \) is returned by \text{CHOOSE}, two recursive calls are made to \text{REFINE}, one with argument \( H \land p \), and the other with argument \( H \land \neg p \).

The refinement algorithm generates commutativity conditions that are in disjunctive normal form: \( \varphi ::= \varphi \lor \varphi \mid (\neg Y) \) where \( Y ::= Y \land Y \mid p \) and \( p \) is from a language of predicates. Hence, any (finite) logical formula can be represented. This logical language is more expressive than previous commutativity logics that, because they were designed for run-time purposes, were restricted to conjunctions of inequalities [16] and boolean combinations of predicates over finite domains [8].

VI. Soundness and Relative Completeness

The following theorem shows that \( \varphi \) is a sound approximation of when \( m \nabla n \) always holds (and similar for \( \hat{\varphi} \)).

**Theorem VI.1** (Soundness). At each iteration of \( \text{REFINE} \), \( \varphi \Rightarrow m \nabla n \), and \( \hat{\varphi} \Rightarrow m \nabla n \).

**Proof.** By induction. Initially, \( false \) is a suitable condition for when commutativity holds, and \( false \) is a suitable condition under which commutativity does not hold. At each iteration, \( \varphi \) or \( \hat{\varphi} \) may be updated (not both, but for soundness this does not matter). Consider \( \varphi \). It must also be the case that \( (\varphi \lor H) \Rightarrow m \nabla n \) because we know that \( \varphi \Rightarrow m \nabla n \) (from the previous iteration) and that \( H \Rightarrow m \nabla n \) (from the valid check on Line 2). Similar reasoning for \( \hat{\varphi} \).

Soundness holds regardless of what \text{CHOOSE} returns (not surprising since updates to \( \varphi \) and \( \hat{\varphi} \) are guarded by validity checks) and even when the theories used to model the underlying data-structure are incomplete. Next we show that termination implies completeness (Lemma VI.2) and give some conditions under which termination, and thus completeness, is ensured (Theorem VI.3).

**Lemma VI.2.** If \( \text{REFINE} \) terminates, then \( \varphi \lor \hat{\varphi} \).

**Proof.** The recursive calls of the \( \text{REFINE} \) algorithm induces a binary tree \( T \), where nodes are labeled by the conjunction of predicates. If \( \text{REFINE} \) terminates, then \( T \) is finite, and each node is labeled with a finite conjunction \( p_0 \land ... \land p_n \).

**Claim.** The disj. of all leaf node labels is valid. \( Pf. \) By induction on the tree. Base case: a single-node tree has label \( true \). Inductive case: for every new node created, labeled with a new conjunct \( \land p \), there is a sibling node with label \( \land \neg p \).

Each leaf node of tree \( T \), labeled with conjunction \( \gamma \) arises from \( \text{REFINE} \) reaching a base case where, by construction, the conjunction \( \gamma \) is disjunctively added to either \( \varphi \) or \( \hat{\varphi} \). Since \( \text{REFINE} \) terminates, all conjunctions are added to either \( \varphi \) or \( \hat{\varphi} \) and, thus, \( \varphi \lor \hat{\varphi} \) must be valid. \( \square \)
Theorem VI.3 (Sufficient Conditions for Termination). Provided that

1) (expressiveness) the state space $\Sigma$ is partitionable into a finite set of regions $\Sigma_1, ..., \Sigma_N$, each described by a finite conjunction of predicates $\psi_i$, such that either $\psi_i \Rightarrow m \triangleright n$ or $\psi_i \Rightarrow m \triangleright n$; and

2) (fairness) for every $p \in P$, CHOOSE eventually picks $p$ (note that this does not imply that $P$ is finite),

then $\text{REFINE}_{n_i}$ terminates.

Proof. By contradiction. As in the proof for Lemma VI.2, we represent the algorithm’s execution as a binary tree $T$, induced by the recursive $\text{REFINE}$ calls, whose nodes are labeled by the conjunction of predicates (lines 9 and 10 in Algorithm 2). Assume there exists an infinite path along $T$, and let their respective labels be $\pi = p_0, p_0 \land p_1, p_0 \land p_1 \land p_2, ....$

Claim. There is no finite prefix of $\pi$ that contains all the predicates $\psi_i$. Pf. Had there been such a prefix $\psi$, by the (expressiveness) assumption the running condition $H$ would satisfy one of the validity checks at lines 2 and 4 within, or immediately after, $\psi$. This is because $H$ would be equal to, or stronger than, the conjunction of the predicates $\psi_i$. This would have made $\pi$ finite, as $\pi$ is extended only if both of the validity checks fail, where we assume $\pi$ is infinite.

By the above claim, at least one of the predicates $\psi_i$ is not contained in any finite prefix of $\pi$. This contradicts the (fairness) assumption, whereby any predicate $p \in P$ is chosen after finitely many CHOOSE invocations (provided the algorithm hasn’t terminated).

Note that, while these conditions ensure termination, the bound on the number of iterations depends on the predicate language and behavior of CHOOSE.

VII. IMPLEMENTATION & EVALUATION

We have implemented the algorithm in Section V, along with the other parts of the system (illustrated in Section II) in our tool SERVOIS†. We use CVC4 [5] as the backend solver. We evaluated the algorithm on various abstract data structures, and discuss some challenges, choices and optimizations in the following.

a) Encoding the transition system: We use an input specification language building on YAML (which has parsers and printers in all common programming languages) with SMTLIB as the logical language. It is human-readable as well as can be easily auto-generated allowing to easily fit in other toolchains [12], [9], [10], [4], [20], [17]. See Appendix B for the Counter ADT specification which was derived from the $Pre$ and $Post$ conditions used in earlier work [13]. The novelty of our work is that we automatically generate commutativity conditions for any implementation that respects these contracts.

The states (state) of a transition system describing an ADT are encoded as list of variables (each as a name, type pair), and each method (methods) specification requires a list of argument types (args), return type (return), and $Pre$ (requires) and $Post$ (ensures) conditions. The full specifications for Counter, Accumulator, Set, HashTable, and Stack we used can be found in the Appendix. We used the quantifier-free integer theory in SMTLIB to encode the abstract state and contracts for the counter and accumulator ADTs. For Set, we used the theory of finite sets along with integers to track size; for HashTable we used sets to track the keys, and arrays for the HashMap itself. In order to encode the Stack example, we utilized the observation that for the purpose of pairwise commutativity it is sufficient to track the behavior of boundedly many top elements. In specific, since two operations can at most either pop the top two elements or push two elements, tracking four elements is sufficient.

b) LIFT: As mentioned in previous sections, our implementation uses an SMT solver to check that the transition system encoding given as input is deterministic and we have implemented the LIFT transformation described in Section IV, which gives a new specification to make sure it is total by construction (see Appendix C for the auto-lifted Counter).

c) PG (predicate generation): One of the questions that arises is how to obtain a relevant set of the predicates. As mentioned in Section II, our intuition was that the commutativity condition would need to involve terms and predicates that are used to describe the methods. Using this intuition, the procedure we use to generate a set of predicates for input to our $\text{REFINE}$ is as follows: (i) take all terms that appear in the input specification of the $Pre$ and $Post$ conditions, grouped by the sort, (ii) take all predicate symbols that appear in the specification, and generate all possible atoms that are well-typed using terms extracted. For example, if $size, 1, (size+1)$ are terms of sort $\mathbb{Z}$ that appear in the formula along with the predicates $= \land \ge$, we generate $(size = 1), (size \ge 1)$, etc. for a total of 18 predicates. We filter out those that are trivial.

By this process, depending on the pair of methods, the number of predicates generated by our implementation of PG were (in parenthesis, after filtering): Counter: 25-25 (12-12), Accumulator: 1-20 (0-20), Set: 17-55 (17-34), HashTable: 18-36 (6-36), Stack: 41-61 (41-42).

d) CHOOSE: Even though the number of predicates obtained is relatively small, our algorithm makes two recursive calls at each step. It is thus important to be able to identify relevant predicates for the algorithm to be practical.

To this end, in addition to filtering trivial predicates, inspired by CEGAR techniques we prioritize predicates based on the two counterexamples generated from the validity checks in $\text{REFINE}$. Predicates that distinguish between the given counterexamples are tried first (call these distinguishing predicates). The property of these predicates is that they ensure both counterexamples can be valid on recursing and thus guarantee progress. More formally, CHOOSE must return a predicate such that $\chi_c \Rightarrow H \land p$ and $\chi_{nc} \Rightarrow H \land \lnot p$. In our implementation, we provided the SMT solver all the predicates upfront, which returns the evaluation on the counterexample without any additional queries. This still left us with several predicates, and we discuss the heuristics we tried to break ties.
<table>
<thead>
<tr>
<th>Meth. ( m(z) )</th>
<th>Meth. ( n(y) )</th>
<th>Simple</th>
<th>poke</th>
<th>( c_{m,n}'' ) generated by poke heuristic</th>
</tr>
</thead>
<tbody>
<tr>
<td>decrement ( \triangleright ) decrement</td>
<td>1 (0.11)</td>
<td>3 (0.11)</td>
<td>true</td>
<td></td>
</tr>
<tr>
<td>increment ( \triangleleft ) decrement</td>
<td>10 (0.36)</td>
<td>34 (0.91)</td>
<td>( \neg (0 = c) )</td>
<td></td>
</tr>
<tr>
<td>decrement ( \triangleright ) increment</td>
<td>3 (0.11)</td>
<td>3 (0.12)</td>
<td>true</td>
<td></td>
</tr>
<tr>
<td>decrement ( \triangleright ) reset</td>
<td>2 (0.10)</td>
<td>2 (0.10)</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>increment ( \triangleright ) increment</td>
<td>3 (0.12)</td>
<td>3 (0.11)</td>
<td>true</td>
<td></td>
</tr>
<tr>
<td>increment ( \triangleright ) reset</td>
<td>2 (0.09)</td>
<td>2 (0.10)</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>increment ( \triangleright ) zero</td>
<td>10 (0.30)</td>
<td>34 (0.86)</td>
<td>( \neg (0 = c) )</td>
<td></td>
</tr>
<tr>
<td>reset ( \triangleright ) reset</td>
<td>3 (0.11)</td>
<td>3 (0.11)</td>
<td>true</td>
<td></td>
</tr>
<tr>
<td>reset ( \triangleright ) zero</td>
<td>9 (0.24)</td>
<td>30 (0.69)</td>
<td>( 0 = c )</td>
<td></td>
</tr>
<tr>
<td>zero ( \triangleright ) zero</td>
<td>3 (0.11)</td>
<td>3 (0.11)</td>
<td>true</td>
<td></td>
</tr>
</tbody>
</table>

**Simple Heuristic.** One relatively simple heuristic we tried was to start by picking the predicates with the least number of terms. The intuition was that conditions would at least involve some simple atoms, and would consequently lead to simple conditions. This worked very well, on all our examples this heuristic terminated with precise commutativity conditions. In Figure 3, we give the number of queries posed to the solver and total time (in parentheses) consumed by this heuristic.

**Poke Heuristic.** Though the simple heuristic produces precise conditions, we now focus on the qualitative aspect of our synthesis algorithm. We found that in some cases the simple \textsc{choose} heuristic would pick predicates to split on that could have been technically avoided in the commutativity condition. Not an issue from correctness point of view, nevertheless, we tried a heuristic which tries more aggressively to find concise conditions in addition to being precise.

![Figure 3](image_url)
and computes number of distinguishing predicates would the two calls have. The sum of values returned by the two calls becomes the weight of the predicate. We then pick the predicate with lowest weight (fewest remaining distinguishing predicates). This heuristic was found to converge much faster to the more relevant predicates. This requires more calls to the SMT solver, but since the queries were relatively simple for CVC4, it was not overall an issue. The conditions in the Figure 3 are those generated by the Poke heuristic. Please see the appendix for a comparison with those generated by Simple heuristic.

On the theoretical side, our CHOOSE implementation is fair (it satisfies condition 2 of Theorem VI.3 as in lines 9-10 of algorithm remove the predicate being tried from P). Also, from our experiments we can conclude that our choice of predicates satisfies condition 1 in VI.3.

e) Validation: Although our algorithm is sound, we manually validated the implementation of SERVOIS by examining its output and comparing the generated commutativity conditions with those manually written in prior works. In the case of the Accumulator and Counter, our commutativity conditions were identical to those given in [13]. For the Set datastructure, the work of [13] used a less precise Set abstraction, so we instead validated against the conditions of [16]. For the HashTable, we validated that our conditions matched those given by Dimitrov et al. [8].

VIII. CONCLUSION

Our work shows that it is possible to automatically generate commutativity conditions, something that was done manually so far. The conditions are correct by construction and ensure special cases aren’t missed. These conditions can be derived statically, and used in a variety of contexts including transactional boosting [11], open nested transactions [21], and other non-transactional concurrency paradigms such as race detection [8], automatic parallelization [22], etc.

REFERENCES

APPENDIX

A. Right-/Left-movers

Definition A.1 (Action right-mover [19]). We say that an action $\alpha_1$ moves to the right of action $\alpha_2$ commute, denoted $\alpha_1 \triangleright \alpha_2$, provided that $\{\alpha_2\} \circ \{\alpha_1\} \subseteq \{\alpha_1\} \circ \{\alpha_2\}$.

Note that left-movers can be defined as right-movers, but with arguments swapped.

Definition A.2 (Method right-mover). For $m$ and $n$,

$$m \triangleright n \equiv \forall \bar{x} \bar{y} \bar{r} \bar{s}. \; m(\bar{x})/\bar{r} \triangleright n(\bar{y})/\bar{s}$$

A logical right-mover condition denoted $\Psi^m_n$ has the same type as a commutativity condition and, again $[\Psi^m_n]$ denotes interpretations of $\Psi^m_n$. Moreover, we say that $\Psi^m_n$ is a right-mover condition for $m$ and $n$ provided that $\forall \sigma_0 \bar{x} \bar{y} \bar{r} \bar{s}. \; [\Psi^m_n] \; \sigma_0 \; (m(\bar{x})/\bar{r}) \; (n(\bar{y})/\bar{s}) = \text{true} \Rightarrow m \triangleright n$ and similar for a non-right-mover condition.

Checking whether $H^m_n \Rightarrow m \triangleright n$. After performing the lifting transformation, we again are able to reduce the question of whether a formula $H^m_n$ is a right-mover condition to a validity check that does not introduce quantifier alternation.

$$\begin{align*}
\forall \bar{\sigma}_0 \bar{x} \bar{y} \bar{r} \bar{s}. \; \\
\; \Psi^m_n(\bar{\sigma}_0, \bar{x}, \bar{y}, \bar{r}, \bar{s}) \Rightarrow \\
\; \bar{\sigma}_0 \neq \text{Err} \Rightarrow \\
\; (\{n(\bar{y})/\bar{s}\} \{m(\bar{x})/\bar{r}\} \bar{\sigma}_0 \neq \text{Err} \Rightarrow \\
\; (\{n(\bar{y})/\bar{s}\} \{m(\bar{x})/\bar{r}\} \bar{\sigma}_0 = (\{m(\bar{x})/\bar{r}\} \{n(\bar{y})/\bar{s}\} \bar{\sigma}_0).
\end{align*}$$

Notice that this is a generalization of the validity check for commutativity.

B. Counter

\# Counter data structure’s abstract definition

name: counter

state:
- name: contents
type: Int

states_equal:
definition: (= contents_1 contents_2)

methods:
- name: increment
  args: [
  return:
  - name: result
type: Bool
requires: [
  (>= contents 0)
ensures: [
  (and (= contents_new (+ contents 1)) (= result true))
  terms:
  Int: [contents, 1, (+ contents 1)]
  - name: decrement
  args: [
  return:
  - name: result
type: Bool
requires: [
  (>= contents 1)
ensures: [
  (and (= contents_new (- contents 1)) (= result true))
  terms:
  Int: [contents, 1, (- contents 1), 0]
  - name: reset
  args: []
return:
  - name: result
type: Bool
requires: [
  (>= contents 0)
ensures: [
  (and (= contents_new 0) (= result true))
  terms:
  Int: [contents, 0]
- name: zero
args: [
return:
  - name: result
type: Bool
requires: [
  (>= contents 0)
ensures: [
  (and (= contents_new contents) (= result true))
  terms:
  Int: [contents, 0]
]

decrements:
  - name: "–"
type: [Int, Int]
    \[ \begin{array}{ll}
    \text{decrement} \triangleright \text{decrement} & \text{Simple: true} \\
    \text{true} & \text{Poke: true} \\
    \text{true} & \text{Poke: false} \\
    \text{false} & \text{Poke: false} \\
    \text{false} & \text{Poke: true} \\
    \text{true} & \text{Poke: true} \\
    \end{array} \]

  \]
C. Counter (lifted, auto-generated)

methods:
- args: []
  ensures: "(or (and err err_new) \n (and (not err) (not err_new)) \n (and (= contents_new (+ contents 1)) \n (and (not err) \n \\ err err_new (not (> contents 0) \n ))))*
name: increment
requires: 'true'
return:
  - name: result
    type: Bool
  terms:
    Int:
      - contents
      - 1
      - (+ contents 1)
- args: []
  ensures: "(or (and err err_new) \n (and (not err) (not err_new)) \n (and (= contents_new (- contents 1)) \n (and (not err) \n \\ err err_new (not (> contents 1) \n )))*
name: decrement
requires: 'true'
return:
  - name: result
    type: Bool
  terms:
    Int:
      - contents
      - 1
      - (- contents 1)
      - 0
- args: []
  ensures: "(or (and err err_new) \n (and (not err) (not err_new)) \n (and (= contents_new 0) \n (and (not err) err_new) \n (and (not (> contents 0) \n )))*
name: reset
requires: 'true'
return:
  - name: result
    type: Bool
  terms:
    Int:
      - contents
      - 0
- args: []
  ensures: "(or (and err err_new) \n (and (not err) (not err_new)) \n (and (= contents_new contents) \n (and (not err) err_new) \n (and (not (> contents 0) \n )))*
name: zero
requires: 'true'
return:
  - name: result
    type: Bool
  terms:
    Int:
      - contents
      - 0
name: counter
predicates:
  - name: '='
type: [Int, Int]

D. Accumulator

# Accumulator abstract definition

name: accumulator
state:
  - name: contents
    type: Int
options:

states_equal:
  definition: (= contents_1 contents_2)

methods:
- name: increase
  args:
    - name: n
type: Int
  return:
    - name: result
      type: Bool
  requires: |
    true
  ensures: |
    (and (= contents_new (+ contents n))
    (= result true))
  terms:
    Int: [n, contents, (+ contents $1)]
- name: read
  args: []
  return:
    - name: result
      type: Int
  requires: |
    true
  ensures: |
    (and (= contents_new (+ contents n))
    (= result contents))
  terms:
    Int: [contents]

predicates:
  - name: "=
    type: [Int, Int]

• increase ⊢ increase
  Simple: true
  Poke: true

• increase ⊢ read
  Simple: [x1 = contents ∧ contents + x1 = contents]
 ∨ [¬(x1 = contents) ∧ contents + x1 = contents]
  Poke: contents + x1 = contents

• read ⊢ read
  Simple: true
  Poke: true

E. Set

name: set

preamble: |
(declare-sort E 0)

state:
  - name: S
    type: (Set E)
methods:
- name: add
  args:
    - name: v
  type: E
  return:
    - name: result
  type: Bool
  requires: |
  return:
  args:
  terms:
  ensures: |
  (ite (member v S)
    (and (= S_new (setminus S (singleton v)))
     (= size_new (- size 1))
     result))
  (and (= S_1 S_2) (= size_1 size_2))
  (not result))
  (and (= S_new (union S (singleton v)))
   (= size_new (+ size 1))
   result))
  (and (= S_new (union S (singleton v)))
   (= size_new (+ size 1))
   result))
  terms:
  E: [E]
  Int: [size, 1, (singleton $1), (union S (singleton $1))]
  - name: remove
    args:
      - name: v
    type: E
    return:
      - name: result
    type: Bool
    requires: |
    true
    returns: |
    (ite (member v S)
      (and (= S_new (setminus S (singleton v)))
       (= size_new (- size 1))
       result))
  terms:
  E: [E]
  Int: [size, 1, (singleton $1), (union S (singleton $1))]
  - name: contains
    args:
      - name: v
    type: E
    return:
      - name: result
    type: Bool
    requires: |
    true
    returns: |
    (ite (member v S)
      (and (= S_new (setminus S (singleton v)))
       (= size_new (- size 1))
       result))
  terms:
  E: [E]
  Int: [size, 1, (singleton $1), (union S (singleton $1))]
  - name: getsize
    args:
    return:
    requires: |
    true
    returns: |
    (ite (member v S)
      (and (= S_new (setminus S (singleton v)))
       (= size_new (- size 1))
       result))
  terms:
  E: [E]
  Int: [size, 1, (singleton $1), (union S (singleton $1))]
  - name: HashTable
    # Hash table data structure’s abstract definition
    F . HashTable
    (declare-sort E 0)
    (declare-sort Int 0)
    (declare-sort Bool 0)
state:
- name: keys
type: (Set E)
- name: H
type: (Array E F)
- name: size
type: Int

states_equal:
definition:
(and (= keys_1 keys_2)
 (= H_1 H_2)
 (= size_1 size_2))

methods:
- name: haskey
  args:
  - name: k0
type: E
  return:
  - name: result
type: Bool
  requires: |
    true
  ensures: |
    (and (= keys_new keys)
     (= H_new H)
     (= size_new size)
     (= (member k0 keys) result))

terms:
Int: [size]
E: [E]
(Set E): [keys]
(Array E F): [H]

- name: remove
  args:
  - name: v
type: E
  return:
  - name: result
type: Bool
  requires: |
    true
  ensures: |
    (ite (member v keys)
      (and (= keys_new \setminus keys (singleton v))
       (= size_new (- size 1))
       (= H_new H)
       (not result))
      (and (= keys_new keys)
       (= size_new size)
       (= H_new H)
       (member k0 keys) result))

terms:
Int: [size]
E: [E]
(Set E): [keys]
(Array E F): [H]

- name: put
  args:
  - name: k0
type: E
  - name: v0
type: F
  return:
  - name: result
type: Bool
  requires: |
    true
  ensures: |
    (ite (member k0 keys)
      (and (= keys_new \insert keys k0 keys)
       (= size_new (+ size 1))
       result)
      (ite (= (store H k0 v0) result)
       (ite (= H_new H)
        (= size_new size)
        (= (select H k0) result))
       (ite (= v0 (select H k0))
        (= (store H k0 v0) result)
        (= (member k0 keys))
        (= (store H k0 v0) result))
      )

  terms:
Int: [size]
E: [E]
F: [F]
(Set E): [keys]
(Array E F): [H]

- name: remove
  args:
  - name: v
type: E
  return:
    - name: result
type: Int
  requires: |
    true
  ensures: |
    (ite (member v keys)
      (and (= keys_new \setminus keys (singleton v))
       (= size_new (- size 1))
       (= H_new H)
       (not result))
      (and (= keys_new keys)
       (= size_new size)
       (= H_new H)
       (= size result))

  terms:
Int: [size]
E: [E]
F: [F]
(Set E): [keys]
(Array E F): [H]

predicates:
- name: "="
type: [Int, Int]
- name: "="
type: [E, E]
- name: "="
type: [F, F]
- name: "="
type: [(Set E), (Set E)]
- name: "="
type: [(Array E F), (Array E F)]
- name: "member"
type: [E, (Set E)]
\[ \forall (\neg [y_1] = y_2) \land \neg (y_1 = x_1) \]

- remove \( \triangleright \) get
  Simple: true
  Poke: true

- get \( \triangleright \) remove
  Simple: 
  \[ [1 = \text{size} \land \neg (y_1 = x_1)] \]
  \[ \forall (\neg [1 = \text{size}] \land \neg (y_1 = x_1))] \]
  Poke: \( \neg (y_1 = x_1) \)

- haskey \( \triangleright \) haskey
  Simple: true
  Poke: true

- haskey \( \triangleright \) put
  Simple: 
  \[ [y_1 \in \text{keys} \land \neg (y_1 = x_1)] \]
  \[ \forall (\neg [y_1 \in \text{keys}] \land \neg (y_1 = x_1))] \]
  Poke: true

- haskey \( \triangleright \) haskey
  Simple: true
  Poke: true

- haskey \( \triangleright \) put
  Simple: 
  \[ [y_1 \in \text{keys} \land \neg (y_1 = x_1)] \]
  \[ \forall (\neg [y_1 \in \text{keys}] \land \neg (y_1 = x_1))] \]
  Poke: true

- haskey \( \triangleright \) remove
  Simple: 
  \[ [y_1 = x_1 \land \neg \text{size} \land \neg (y_1 \in \text{keys})] \]
  \[ \forall ([y_1 = x_1] \land \neg (y_1 \in \text{keys})] \]
  \[ \forall (\neg [y_1 = x_1]) \]

- remove \( \triangleleft \) size
  Simple: true
  Poke: true

- size \( \leq \) size
  Simple: true
  Poke: true

G. Stack

// Stack definition

name: stack

preamble: |
(declare-sort E 0)

state:
- name: size
  type: Int
- name: top
  type: E
- name: nextToTop
  type: E
- name: secondToTop
  type: E
- name: thirdToTop
  type: E

states_equal:
  definition:
  (and (= size_1 size_2)
   (or (= size_1 0) (= size_1 1) (= top_1 top_2))
   (and (= size_1 (+ size_1 1))
    (= nextToTop_1 nextToTop_2)))))

methods:
- name: push
  args: |
    - name: v
    - type: E
  return:
    - name: result
      type: Bool
  requires: |
    (>= size 0)
  ensures: |
    (and (= size_new (+ size 1))
     (= top_new v)
     (= nextToTop_new top)
     (= secondToTop_new nextToTop)
     (= thirdToTop_new secondToTop)
     (= result true))

terms:
  Int: [size, 1, (+ size 1)]
  E: [top, nextToTop, secondToTop, thirdToTop, $1]

- name: pop
  args: []
  return:
    - name: result
      type: E
  requires: |
    (>= size 1)
  ensures: |
    (and (= size_new (- size 1))
     (= result top)
     (= top_new nextToTop)
     (= secondToTop_new secondToTop)
     (= thirdToTop_new thirdToTop))

terms:
  Int: [size, 1, (- size 1), 0]
  E: [top, nextToTop, secondToTop, thirdToTop]

- name: clear
  args: []
  return:
    - name: result
      type: Bool
  requires: |
    (>= size 0)
  ensures: |
    (and (= size_new 0)
     (= result true))

terms:
  Int: [size, 0]
E: [top, nextToTop, secondToTop, thirdToTop]

predicates:
- name: "="
type: [Int, Int]
- name: "="
type: [E, E]

- clear ⊢ clear
  Simple: true
  Poke: true

- clear ⊢ pop
  Simple: false
  Poke: false

- clear ⊢ push
  Simple: false
  Poke: false

- pop ⊢ pop
  Simple: nextToTop = top
  Poke: nextToTop = top

- push ⊢ pop
  Simple:
  \[1 = \text{size} \land \text{nextToTop} = \text{top} \land \text{nextToTop} = \text{thirdToTop} \land \text{nextToTop} = x1\]
  \[\lor 1 = \text{size} \land \text{nextToTop} = \text{top} \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \text{nextToTop} = x1\]
  \[\lor 1 = \text{size} \land \neg(\text{nextToTop} = \text{top}) \land \text{nextToTop} = \text{thirdToTop} \land \text{nextToTop} = \text{secondToTop} \land \text{top} = x1\]
  \[\lor 1 = \text{size} \land \neg(\text{nextToTop} = \text{top}) \land \text{nextToTop} = \text{thirdToTop} \land \neg(\text{nextToTop} = \text{secondToTop}) \land \text{top} = x1\]
  \[\lor 1 = \text{size} \land \neg(\text{nextToTop} = \text{top}) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \text{top} = x1\]
  \[\lor \neg(0 = \text{size}) \land \neg(0 = \text{size}) \land \text{nextToTop} = \text{thirdToTop} \land \neg(\text{nextToTop} = \text{secondToTop}) \land \text{top} = x1\]
  \[\lor \neg(1 = \text{size}) \land \neg(0 = \text{size}) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \neg(\text{nextToTop} = \text{secondToTop}) \land \text{top} = x1\]
  \[\lor \neg(1 = \text{size}) \land \neg(0 = \text{size}) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \neg(\text{nextToTop} = \text{secondToTop}) \land \text{top} = x1\]
  \[\lor \neg(1 = \text{size}) \land \neg(0 = \text{size}) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \neg(\text{nextToTop} = \text{secondToTop}) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \text{top} = x1\]
  \[\lor \neg(1 = \text{size}) \land \neg(0 = \text{size}) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \neg(\text{nextToTop} = \text{secondToTop}) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \neg(\text{top} = x1)\]
  Poke:
  \[\neg(0 = \text{size}) \land \text{top} = x1\]

- pop ⊢ push
  Simple:
  \[\neg(\text{nextToTop} = y1) \land \text{nextToTop} = \text{thirdToTop} \land \text{nextToTop} = y1 \land \text{top}\]
  \[\lor \neg(\text{nextToTop} = y1) \land \text{nextToTop} = \text{thirdToTop} \land \neg(\text{nextToTop} = \text{secondToTop}) \land y1 = \text{top}\]
  \[\lor \neg(\text{nextToTop} = y1) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \text{nextToTop} = \text{secondToTop} \land y1 = \text{top}\]
  \[\lor \neg(\text{nextToTop} = y1) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \neg(\text{nextToTop} = \text{secondToTop}) \land y1 = \text{top}\]
  \[\lor \neg(\text{nextToTop} = y1) \land \neg(\text{nextToTop} = \text{thirdToTop}) \land \neg(\text{nextToTop} = \text{secondToTop}) \land y1 = \text{top}\]
  Poke:
  \[y1 = \text{top}\]

- push ⊢ push
  Simple:
  \[\neg(\text{thirdToTop} = y1) \land \text{thirdToTop} = x1\]
  \[\lor \neg(\text{thirdToTop} = y1) \land y1 = x1\]
  Poke:
  \[y1 = x1\]