

Curriculum Vitae

Ruzica Piskac

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Research Interests

Programming languages, software engineering, program synthesis, verification, automated reasoning

Work Experience

- [09/2013 – present] Yale University, New Haven, CT, USA
 - assistant professor, tenure-track
- [01/2012 – 08/2013] Max Planck Institute for Software Systems (MPI-SWS), Germany
 - independent research group leader
- [June – September 2008] Microsoft Research, Redmond, WA, USA
 - summer internship, mentor: Nikolaj Bjørner
- [08/2006 – 08/2007] Digital Enterprise Research Institute (DERI), Innsbruck, Austria
 - junior researcher and teaching assistant
- [12/2000 – 09/2002] University of Zagreb, Croatia
 - junior researcher and teaching assistant, part-time intern at CERN, Geneva

Education

- [09/2007 – 12/2011] Swiss Federal Institute of Technology (EPFL), Lausanne, Switzerland
 - PhD student at EPFL, Laboratory for Automated Reasoning and Analysis (LARA) group
 - thesis title: “Decision Procedures for Program Synthesis and Verification”, no. 5220 (2011)
 - thesis advisor: Viktor Kuncak
- [10/2002 – 08/2006] Max-Planck-Institut für Informatik, Saarbrücken, Germany
 - master student at MPII, Programming Logics Group, advisors: Harald Ganzinger[†], Andreas Podelski, Hans de Nivelle
 - Master’s thesis: “Formal Correctness of Result Checking for Priority Queues”
 - finished Master program with the best grades (1.0/1.0)
- [09/1995 – 12/2000] University of Zagreb, Croatia
 - student of mathematics at the University of Zagreb, Croatia, advisor: Robert Manger
 - Diploma thesis: “Parallel Algorithms for Sorting and Merging” (in Croatian)
 - Dipl.Ing. degree with Honors, graduated as the first in the class of 81 students

Honors and Awards

- The Microsoft Research Award for the Software Engineering Innovation Foundation (SEIF) 2014, \$40,000
- Elected Member of Elisabeth-Schiemann-Kolleg, Max Planck Society, 2014
- The Patrick Denantes Prize 2012 winner, awarded annually to “an outstanding Master, Doctoral or Post-doc project from the I&C School” (the school of computer and communication sciences at EPFL)
- *Research Highlight* paper published in Communications of the ACM (55), Volume 55, Number 2, February 2012
- Rising Stars in EECS: An Academic Career Workshop for Women, MIT, Cambridge, MA, 2012 – *by invitation only*
- The Google Anita Borg Memorial Scholarship 2010 winner
- Grace Hopper Conference attendance full scholarship recipient 2008 and 2007
- [2002 – 2006] IMPRS Fellowship for both Master and Ph.D. studies in Computer Science. Max-Planck Institut für Informatik, Saarbrücken, Germany
- Won the most prestigious student’s award of Croatia: Chancellor’s Award (“Rektorova nagrada”), 2000 for the work in the area of decision making theory

Grants

- *CAREER: Synthesis in a Live Programming Environment*, \$179,488, awarded by NSF (CCF-1553168)
- *SHF: Medium: Collaborative Research: FRP for Real*, \$866,000, awarded by NSF (CCF-1302327) originally to Paul Hudak
- *Student Travel Support for SAT/SMT/AR Summer School at IJCAR 2016*, \$30,000, awarded by NSF (CCF-1636493)
- *Student Travel Support for VMCAI 2015*, \$19,992, awarded by NSF (CCF-1515943)
- *Student Travel Support for POPL 2015*, \$25,000, awarded by NSF (CCF-1451760)
- *Student Travel Support for VMCAI 2014*, \$15,000, awarded by NSF (CCF-1401905)

Publications

Conferences

1. M. Santolucito, E. Zhai, R. Piskac: **Probabilistic Automated Language Learning for Configuration Files**. *Proceedings of the 28th International Conference on Computer Aided Verification (CAV 2016)*, p. 80-87.
2. A. Reinking, R. Piskac: **A Type-Directed Approach to Program Repair**. *Proceedings of the 27th International Conference on Computer Aided Verification (CAV 2015)*, p. 511-517.
3. S. Gulwani, Mikael Mayer, F. Niksic, R. Piskac: **StriSynth: Synthesis for Live Programming**. *Proceedings of the ICSE 2015 Demonstrations Track*, p. 701-704.
4. T. Gvero, V. Kuncak, I. Kuraj, R. Piskac. **InSynth: A System for Code Completion using Types and Weights**. *Proceedings of the Software Engineering & Management 2015* Dresden, Germany, p. 39-40
5. R. Piskac: **The FMCAD 2014 graduate student forum**. *Proceedings of the International Conference on Formal Methods in Computer-Aided Design (FMCAD 2014)*, p. 13.
6. R. Piskac, T. Wies, D. Zufferey: **Automating Separation Logic with Trees and Data**. *Proceedings of the 26th International Conference on Computer Aided Verification (CAV 2014)*, p. 711-728.
7. R. Piskac, T. Wies, D. Zufferey: **GRASShopper: Complete Heap Verification with Mixed Specifications**. *Proceedings of the 20th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2014)*, p. 124-139.
8. J. Kloos, R. Majumdar, F. Niksic, R. Piskac: **Incremental, Inductive Coverability**. *Proceedings of the 25th International Conference on Computer Aided Verification (CAV 2013)*, Springer, LNCS Volume 8044, p. 158-173.
9. R. Piskac, T. Wies, D. Zufferey: **Automating Separation Logic Using SMT**. *Proceedings of the 25th International Conference on Computer Aided Verification (CAV 2013)*, Springer, LNCS Volume 8044, p. 773-789.
10. T. Hillenbrand, R. Piskac, U. Waldmann, C. Weidenbach. **From Search to Computation: Redundancy Criteria and Simplification at Work..** *Proceedings of the Programming Logics - Essays in Memory of Harald Ganzinger*, Springer, LNCS Volume 7797, p. 169-193.

11. T. Gvero, V. Kuncak, I. Kuraj, R. Piskac. **Complete Completion using Types and Weights**. *Proceedings of the ACM SIGPLAN 2013 Conference on Programming Language Design and Implementation (PLDI)*, p. 27-38.
12. T. Gvero, V. Kuncak, R. Piskac. **Interactive Synthesis of Code Snippets**. *Proceedings of the 23rd International Conference on Computer Aided Verification (CAV 2011)*, Springer, LNCS Volume 6806, p. 418-423.
13. R. Piskac, T. Wies. **Decision Procedures for Automating Termination Proofs**. *Proceedings of the 12th International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI 2011)*, Springer, LNCS Volume 6538, p. 371-386.
14. V. Kuncak, R. Piskac, P. Suter. **Ordered Sets in the Calculus of Data Structures**. *Proceedings of the 19th Annual Computer Science Logic (CSL 2010)*, Springer, LNCS Volume 6247, p. 34-48.
15. R. Piskac, V. Kuncak. **MUNCH - Automated Reasoner for Sets and Multisets**. *Proceedings of the 5th International Joint Conference on Automated Reasoning (IJCAR 2010)*, Springer, LNAI 6173, p. 149-155.
16. V. Kuncak, M. Mayer, R. Piskac, P. Suter. **Comfusy: A Tool for Complete Functional Synthesis**. *Proceedings of the 22nd International Conference on Computer Aided Verification (CAV 2010)*, Springer, LNCS Volume 6174, p. 430-433.
17. V. Kuncak, M. Mayer, R. Piskac, P. Suter. **Complete Functional Synthesis**. *Proceedings of the ACM SIGPLAN 2010 Conference on Programming Language Design and Implementation (PLDI)*, p. 316-329.
18. V. Kuncak, R. Piskac, P. Suter, T. Wies: **Building a Calculus of Data Structures**. *Proceedings of the 11th International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI 2010)*, Springer, LNCS Volume 5944, p. 26-44.
19. K. Yessenov, R. Piskac, V. Kuncak. **Collections, Cardinalities, and Relations**. *Proceedings of the 11th International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI 2010)*, Springer, LNCS Volume 5944, p. 380-395.
20. T. Wies, R. Piskac, V. Kuncak. **Combining Theories with Shared Set Operations**. *Proceedings of the 7th International Symposium on Frontiers of Combining Systems (FroCoS 2009)*, Springer, LNCS Volume 5749, p. 366-382.
21. R. Piskac, V. Kuncak. **Fractional Collections with Cardinality Bounds**. *Proceedings of the 17th Annual Computer Science Logic (CSL 2008)*, Springer, LNCS Volume 5213, p. 124-138.
22. R. Piskac, V. Kuncak. **Linear Arithmetic with Stars**. *Proceedings of the 20th International Conference on Computer Aided Verification (CAV 2008)*, Springer, LNCS 5123, p. 268-280.
23. R. Piskac, V. Kuncak. **Decision Procedures for Multisets with Cardinality Constraints**. *Proceedings of the Ninth International Conference on Verification, Model Checking and Abstract Interpretation (VMCAI 2008)*, Springer, LNCS Volume 4905, p. 218-232.
24. H. de Nivelles, R. Piskac. **Verification of an Off-Line Checker for Priority Queues**. *Proceedings of the Third IEEE International Conference on Software Engineering and Formal Methods (SEFM 2005)*, IEEE computer society press, 210-219.
25. L. Caklovic, R. Piskac, V. Segó. 2001. **Improvement of AHP method**. *Mathematical Communications - Supplement No.1 (2001)*, 13-21. (Proceedings of the 8th International Conference on Operational Research - KOI 2000)

Journals

1. V. Kuncak, M. Mayer, R. Piskac, P. Suter. **Functional Synthesis for Linear Arithmetic and Sets**. *STTT* 15(5-6): 455-474 (2013).
2. V. Kuncak, M. Mayer, R. Piskac, P. Suter. **Software Synthesis Procedures**. *Communications of the ACM* (55), Volume 55, Number 2, p. 103-111, DOI: 10.1145/2076450.2076472 February 2012.
3. R. Piskac, L. de Moura, N. Bjørner. **Deciding Effectively Propositional Logic Using DPLL and Substitution Sets**, *Journal of Automated Reasoning*, DOI: 10.1007/s10817-009-9161-6, Volume 44, Number 4, p. 401-424, April 2010.
4. P. Saiz, L. Aphetche, P. Buncic, R. Piskac, J.-E. Revsbech, V. Segó. 2003. **AliEn-ALICE environment on the GRID**. *Nuclear Instruments and Methods in Physics Research Section A*, Volume 502, Issue 2-3, p. 437-440.

Theses

1. **Decision Procedures for Program Synthesis and Verification**. *PhD Thesis*. École Polytechnique Fédérale de Lausanne (EPFL), Switzerland, December 2011.
2. **Formal Correctness of Result Checking for Priority Queues**. *Master Thesis*. Universität des Saarlandes, Saarbrücken, Germany, February 2005.

Technical Reports

1. S. Mitrovic, R. Piskac, V. Kuncak **Identifying Maximal Non-Redundant Integer Cone Generators**, Technical Report, available at <http://lara.epfl.ch/w/nicg>, September 2011.
2. R. Piskac, L. de Moura, N. Bjørner. **Deciding Effectively Propositional Logic with Equality**, Technical Report, MSR-TR-2008-181, December 2008.
3. N. Steinmetz, R. Piskac, C. Feier, U. Keller and H Lausen. D1.3 Report on reasoning techniques and prototype implementation for the WSML-RL and WSML-FOL languages. (published within RW2 project), July 2007.

Proceedings Editor

1. T. King, R. Piskac. Proceedings of the 14th International Workshop on Satisfiability Modulo Theories affiliated with the International Joint Conference on Automated Reasoning, SMT@IJCAR 2016, Coimbra, Portugal, July 1-2, 2016.
2. R. Piskac, R. Dimitrova: Proceedings of the 5th Workshop on Synthesis, SYNT@CAV 2016, Toronto, Canada, July 17-18, 2016.
3. R. Piskac, F. van Harmelen, N. Zhong. Proceedings of the 6th International Semantic Web Conference and the 2nd Asian Semantic Web Conference Workshop on New forms of reasoning for the Semantic Web: scalable, tolerant and dynamic, Busan, Korea, November 2007.
4. F. van Harmelen, A. Herzig, P. Hitzler, Z. Lin, R. Piskac, G. Qi. Proceedings of the 5th European Semantic Web Conference Workshop on Advancing Reasoning on the Web: Scalability and Commonsense (ARea-2008), Tenerife, Spain, June, 2008.
5. R. Piskac, E. Simperl. Proceedings of the Doctoral Consortium of the 3rd Future Internet Symposium 2010, Berlin, Germany, September 23-24, 2010.

Professional Activities

- Organizer and PC co-Chair

1. *The 16th Conference on Formal Methods in Computer-Aided Design (FMCAD 2016)*, Mountain View, CA, USA. Co-organizer with M. Talupur
2. *The 5th Workshop on Synthesis (SYNT 2016)*, co-located with CAV 2016, Toronto, Canada. Co-organizer with R. Dimitrova
3. *The Verification Mentoring Workshop (VMW 2016)*, held with CAV 2016, Toronto, Canada. Co-organizer with A.Gupta, and A. Rybalchenko
4. *International Summer School on Satisfiability, Satisfiability Modulo Theories, and Automated Reasoning*, held in Lisbon, Portugal, before IJCAR 2016. Co-organizer with I. Lynce and P. Rümmer
5. *The 14th International Workshop on Satisfiability Modulo Theories Workshop (SMT 2016)*. Co-organizer with T. King
6. *The 5th Workshop on Synthesis (SYNT 2016)*, to be held with CAV 2016, Toronto, Canada. Co-organizer with R. Dimitrova
7. *The Verification Mentoring Workshop (VMW 2015)*, held with CAV 2015, San Francisco, USA. Co-organizer with A.Gupta, R. Majumdar and C. Pasareanu
8. *The 2015 ACM SIGPLAN Programming Languages Mentoring Workshop (PLMW)*, co-located with POPL 2015, January 2015, Mumbai, India. Co-organizer with D. Dreyer, A. Kanade, A. Schmitt and R. Tate
9. *FMCAD 2014 Student Forum*, co-located with Formal Methods in Computer-Aided Design (FMCAD 2014), Lausanne, Switzerland, October 2014. Organizer.
10. *Doctoral Consortium of the 3rd Future Internet Symposium 2010*, Berlin, Germany, September 2010. Education co-Chair together with E. Simperl
11. *Advancing Reasoning on the Web: Scalability and Commonsense*, workshop co-located with the 5th European Semantic Web Conference, Tenerife, Spain, June 2008. Co-organizer with F. van Harmelen, A. Herzig, P. Hitzler, Z. Lin and G. Qi
12. *New forms of reasoning for the Semantic Web: scalable, tolerant and dynamic*, workshop co-located with the 6th International Semantic Web Conference and the 2nd Asian Semantic Web Conference, Busan, Korea, November 2007. Co-organizer with F. van Harmelen and N. Zhong

- Publicity Chair
 1. The 43rd ACM SIGPLAN-SIGACT Symposium on *Principles of Programming Languages (POPL 2016)*
 2. The Publication Chair for the Formal Methods in Computer-Aided Design conference (FMCAD 2015), to be held in Austin, TX, USA.
 3. The Outreach Activities Chair for the 16th International Conference on *Verification, Model Checking, and Abstract Interpretation (VMCAI 2016)*
 4. The 42nd ACM SIGPLAN-SIGACT Symposium on *Principles of Programming Languages (POPL 2015)*, jointly with Viktor Vafeiadis
 5. The 15th International Conference on *Verification, Model Checking, and Abstract Interpretation (VMCAI 2014)*
- Program Committees
 1. The 2nd Summit on Advances in Programming Languages (SNAPL 2017)
 2. The 6th Workshop on Synthesis (SYNT 2017)
 3. 26th International Conference on Automated Deduction (CADE 2017)
 4. The 26th European Symposium on Programming (ESOP 2017)
 5. The 28th International Conference on Computer-Aided Verification (CAV 2016)
 6. The International Conference on Distributed Computing and Internet Technology (ICDCIT 2016)
 7. The 6th International Symposium on Games, Automata, Logics and Formal Verification (GandALF 2015)
 8. The 13th International Workshop on Satisfiability Modulo Theories (SMT 2015)
 9. The 15th Conference on Formal Methods in Computer-Aided Design (FMCAD 2015)
 10. The 25th International Conference on Automated Deduction (CADE 2015)
 11. Programming Language Design and Implementation (PLDI 2015)
 12. The 21st International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2015)
 13. The 16th International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI 2015)
 14. The 10th Haifa Verification Conference 2014 (HVC 2014)
 15. ESEC/FSE 2014 Tool Demos track
 16. The 14th Conference on Formal Methods in Computer-Aided Design (FMCAD 2014)
 17. The 6th International Symposium on Symbolic Computation in Software Science (SCSS 2014)
 18. The 5th Workshop on Practical Aspects of Automated Reasoning (PAAR-2014)
 19. Programming Language Design and Implementation (PLDI 2014), External Review Committee member
 20. The 15th International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI 2014)
 21. The 13th Conference on Formal Methods in Computer-Aided Design (FMCAD 2013)
 22. ESEC/FSE 2013 Tool Demos track
 23. The 22nd EACSL Annual Conferences on Computer Science Logic (CSL 2013)
 24. The 14th International Workshop on Verification of Infinite-State Systems (INFINITY 2012)
 25. The 3rd Workshop on Practical Aspects of Automated Reasoning (PAAR-2012)
 26. The 18th International Conference on Logic for Programming Artificial Intelligence and Reasoning (LPAR-18)
 27. STI International Symposium 2010 - Organizing Committee Member
 28. The 4th Asian Semantic Web Conference (ASWC 2009)
 29. The 3rd Asian Semantic Web Conference (ASWC 2008)
 30. The Second International Workshop on New forms of reasoning for the Semantic Web: scalable, tolerant and dynamic (NeForS08)

- Reviewer for
 - The COMLAN (Computer Languages, Systems and Structures) journal, The TOSEM (Transactions on Software Engineering and Methodology) journal, Handbook of Model Checking, International Workshop on Verification of Infinite-State Systems (INFINITY 2012), International Workshop on Computer Science Logic (CSL 2013, CSL 2012), Workshop on Practical Aspects of Automated Reasoning (PAAR-2012), Journal of Symbolic Computation, STTT Journal, International Conference on Automated Deduction (CADE 2011), European Symposium on Programming (ESOP2011), Symposium on Principles of Programming Languages (POPL 2011), International Conference on Computer Aided Verification (CAV 2014, 2013, 2012, 2011, 2010), Computer Science Symposium in Russia (CSR 2010), Static Analysis Symposium (SAS 2012, SAS 2011, SAS 2009), Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI 2012, VMCAI 2009), Asian Semantic Web Conference (ASWC 2009, ASWC 2008), Conference on Logic for Programming Artificial Intelligence and Reasoning (LPAR-12, LPAR-08, LPAR-07), International Joint Conference on Automated Reasoning (IJCAR-08), Workshop on Web Semantics (WebS2007), International World Wide Web Conference (WWW2007), Conference on Information Integration and Web-based Applications & Services (iWAS2006)
- [September 2008 - present] Managing editor and publisher of the CEUR-WS.org proceedings series (<http://CEUR-WS.org/>)
 - over 1750 published conference and workshop proceedings

Presentations

- Conference Presentations
 - CAV 2011, SMT 2011, POPL 2011 (student session), IJCAR 2010, CSL 2008, CAV 2008, VMCAI 2008
- Invited Talks and Meetings
 - VMW 2016 (Verification Mentoring Workshop)
 - SYNASC 2015 (Symposium on Symbolic and Numeric Algorithms for Scientific Computing)
 - Microsoft Research Faculty Summit 2014
 - CMACS/AVACS Workshop & PI Meeting, Carnegie Mellon University, Pittsburgh, PA (2013)
 - SYNT 2013, 2nd Workshop on Synthesis, co-located with CAV, Saint Petersburg, Russia (2013)
 - The IMPECS Workshop on Program Analysis, Bangalore, India (2012)
 - “CP meets CAV”, Turunc, Turkey (2012)
 - Microsoft Research Redmond (2013, 2008)
 - NEC Laboratories, Princeton (2012)
 - LogicBlox, Atlanta (2011)
 - Dagstuhl Seminars: 16131, 15442, 14442, 14351, 13502, 13411, 12152, 12461, 10161, 09271, 07401, 05431
 - Other invited talks include: University of Koblenz (2017), University of Innsbruck (2017), University of Graz (2016), Smith College(2014), Wesleyan University(2014), University of Pennsylvania (2013), National University of Ireland (2013), IIT Kanpur (2012), Microsoft Research India, Bangalore (2012), STI Austria (2012, 2010), IST Austria (2012), NYU (2012, 2011), Caltech (2011), Microsoft Research Cambridge (2011), University of Freiburg, Germany (2011, 2006), Université Libre de Bruxelles (2010), The IMDEA Software Institute, Madrid (2010), Colorado University, Boulder (2008), SRI International (2008)
 - Meetings of various European initiatives:
 - * Meetings of the COST actions: Turin (2011), Saarbrücken (2011), Belgrade (2010), Eindhoven (2009)
 - * Alpine Verification Meetings: Lugano (2010), Semmering, Austria (2008)
 - * German Deduction Meeting: Koblenz (2005), Saarbrücken (2004)
 - * The Ringberg Castle seminars: “Deduction at Scale” (2011), Logic Seminar (2003)
- Poster Presentations
 - presented posters at the “Rising Stars in EECS: An Academic Career Workshop for Women”, an invitational workshop at MIT, 2012, the Verification Technology, Systems & Applications Summer School, Luxembourg, 2010, EPFL Research Day (2010 and 2008), Microsoft Research Summer School, Cambridge (2010), The Technical Poster Competition of The Grace Hopper Celebration (2008) - one of four posters that was qualified in the finals, all together more than 100 submissions; “Femmes de Sciences” at EPFL (2008)

Teaching and Supervising Experience

- Courses designed and taught
 - *Software Engineering (CPSC 439/539)*, Yale, Spring 2014, 2015
 - *Software Analysis & Verification (CPSC 454/554)*, Yale, Fall 2014, 2015
 - *Seminar on Software Synthesis*, Yale, Summer 2015
 - *Seminar on Decision Procedures (CPSC 729a)*, Yale, Fall 2013
 - *Embedded Systems*, University of Saarland, Germany, Summer 2014
 - *Seminar on Decision Procedures*, University of Saarland, Fall 2012
- Summer / Winter Schools
 - lecturer at the Summer School on Verification Technology, Systems and Applications (VTSA 2016), Liège, September 2016.
 - lecturer at the *10th International Winter School on MOdelling and VERifying parallel Processes (MOVEP 2012)*, Marseille, December 2012.
 - lecturer (Arbeitsgruppe 3 bei der Akademie XII der Studienstiftung des deutschen Volkes). Topic: *Kann der Computer selbst programmieren?* (synthesis), Papenburg, Spring 2012.
 - lecturer at the *First International SAT/SMT Solver Summer School 2011*, MIT, June 2011.
- Supervised Graduate Students at Yale
 - Mark Santolucito (2015 -) Thesis topic: *Synthesis for Functional Reactive Programming*
 - Bill Hallahan (2016 -) Thesis topic: *Programming by Example in Live Programming Environment*
- Supervised Undergraduate Students at Yale (CPSC 290 and 490)
 - Fall 2016: Derek Lo, Christopher Wan
 - Spring 2016: Nathaya Darakananda, Tihomir Elek, Nick Fieschko, Timothy Follo, Aaron Shim, Jonathon Cai
 - Fall 2015: Evan Hellmuth, Graham Kaemmer, Marvin Qian, Nick Fieschko, Stephen Hemenway, Tihomir Elek
 - Spring 2015: Akshay, Nathan, Daniel Dickstein, Hunter Ford, Ivan Matej Kolobaric, Earl Lee, John Orland, Alexander Reinking, Mikayla Thompson
 - Fall 2014: Feridun Mert Celebi, Abhishek Chandra, Paul Fletcher-Hill, Alexander Reinking, Mikayla Thompson
- Supervised PhD Students at the MPI-SWS Institute, later supervised by Rupak Majumdar
 - Filip Niksic
 - Susanne van den Elsen
- Doctoral Thesis Reviewer and Examiner:
 - David Costanzo: *Formal End-to-End Verification of Information-Flow Security for Complex Systems*, Yale, 2016.
 - Ronghui Gu: *An Extensible Architecture for Building Certified Sequential and Concurrent OS Kernels*, Yale, 2016.
 - Shu-Chun Weng: *DeepSpec: Modular Certified Programming with Deep Specifications*, Yale, 2015.
 - Daniel Winograd-Cort: *Effects, Asynchrony, and Choice in Arrowized Functional Reactive Programming*, Yale, 2015.
- Master Thesis Reviewer and Examiner:
 - Heinrich Ody: *Synthesis of Distributed Reactive Programs*, Saarland University, Germany, November 2013.
 - Ivan Kuraj: *Interactive Code Generation*, EPFL, Switzerland, January 2012.
- Supervised Master Students
 - Mikael Mayer: “Complete Program Synthesis for Linear Arithmetics”, Master Thesis, EPFL, 2010.

- Academic Outreach

- teaching at Yale YES-W 2016 (Yale Engineering and Science Weekend) for high school students and their parents
- giving a talk at a Connecticut Computer Science Teachers Association (CTCSTA) 2016 meeting for high school computer science teachers
- organizing one-day symposiums “Yale PL Day 2016” and “Yale PL Day 2015”
- invited professor in the program “Professors at Large” at the University of Saarland (summer 2014): as a part of the visit, Ruzica organized and supervised weekly meetings of women in the STEM fields. They prepared a poster exhibition on a topic “Women in Computer Science that inspire us”
- invited speaker at *A Yale Celebration of Women in Computing*, January 2014.
- teaching at *cSplash*, NYU - an event for mathematically inclined high school students to attract them to study science and engineering

Language Skills

- Croatian, English, German, Russian